

## VSMP Series (Z-Foil) (0603, 0805, 1206, 1506, 2010, 2018, 2512)

USA Manufacturer of Precision Resistors featuring Bulk Metal<sup>®</sup> Foil

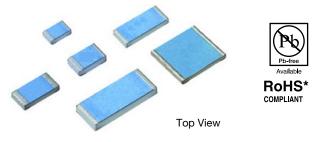
## Ultra High Precision Bulk Metal<sup>®</sup> Foil Wraparound Surface Mount Chip Resistor with TCR as low as ±0.05 ppm/°C and a Power Coefficient of 5 ppm at Rated Power and Load Life Stability of ±0.005% (50 ppm)

### FEATURES

- Temperature coefficient of resistance (TCR): As low as 0.05 ppm/°C (0 °C to + 60 °C) and 0.2 ppm/°C (- 55 °C to + 125 °C, + 25 °C ref.) See Table 1 for details and limits.
- Resistance tolerance: to ±0.01%
- Power coefficient "ΔR due to self heating": 5 ppm at rated power
- Power rating: to 750 mW at +70°C (see table 3)
- Load life stability: to ±0.005% at 70°C, 2000 h at rated power
- Resistance range: 5  $\Omega$  to 125 k $\Omega$  (for lower or higher values, please contact us)
- Bulk Metal<sup>®</sup> Foil resistors have no minimum order requirement and are not restricted to standard values, we can supply specific "as required" values at no extra cost or delivery (e.g. 1K2345 vs. 1K)
- Thermal stabilization time <1 s (nominal value achieved within 10 ppm of steady state value)
- Electrostatic discharge (ESD) at least to 25 kV
- Short time overload: ≤0.005%
- Non inductive, non capacitive design
- Rise time: 1 ns effectively no ringing
- Current noise: 0.010  $\mu V_{\text{RMS}}/V$  of applied voltage (<-40 dB)
- Voltage coefficient <0.1 ppm/V (resistance values above 10 kΩ)
- Non inductive: <0.08 μH</li>
- Non hot spot design
- Terminal finishes available: lead (Pb)-free, tin/lead alloy
- · Matched sets are available on request
- Screening in accordance with EEE-INST-002 and MIL-PRF-55342 available (see datasheet resistor models 303133 to 303138)

Table 1 – Tolerance and TCR vs. ResistanceValue(1) (-55°C to +125°C, +25°C Ref.)						
Resistance value	Tolerance	Typical TCR and max. Spread				
250 Ω to 125 kΩ	±0.01%	±0.2 ±1.8 ppm/°C				
100 Ω to <250 Ω	±0.02%	±0.2 ±1.8 ppm/°C				
50 Ω to <100 Ω	±0.05%	±0.2 ±2.8 ppm/°C				
25 Ω to <50 Ω	±0.1%	±0.2 ±3.8 ppm/°C				
10 Ω to <25 Ω	±0.25%	±0.2 ±3.8 ppm/°C				
5 Ω to <10 Ω	±0.5%	±0.2 ±7.8 ppm/°C				

<sup>(1)</sup> For tighter performances and non-standard values lower than 5 Ω and above 125 kΩ, please contact application engineering using the e-mail addresses in the footer below



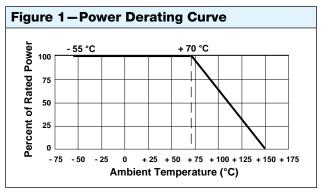
### INTRODUCTION

The VSMP Series chip resistor is the industry's first device to provide high rated power and excellent load life stability along with extremely low TCR – all in one resistor.

One of the most important parameters influencing stability is the Temperature Coefficient of Resistance (TCR). Although the TCR of Bulk Metal® Foil is considered extremely low, this characteristic has been further refined over the years. The VSMP Series utilizes ultra high precision Z Foil. The Z Foil technology provides a significant reduction of the resistive element's sensitivity to ambient temperature variations (TCR) and to self heating when power is applied (Power Coefficient of Resistance, or PCR). Along with the inherently low PCR and TCR, Z Foil technology also provides remarkably improved load life stability, low noise and tight tolerances.

VSMP Series chip resistors have a full wraparound termination which assures secure contact during the manufacturing process, as well as providing stability during multiple thermal cyclings.

Our application engineering department is available to advise and make recommendations. For non-standard technical requirements and special applications. Please contact us using the e-mail address in the footer below.



\* This datasheet provides information about parts that are RoHS-compliant and/or parts that are non-RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS compliant. Please see the information/tables in this datasheet for details.

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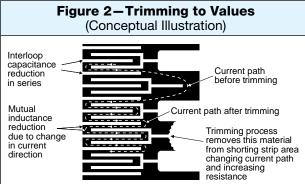
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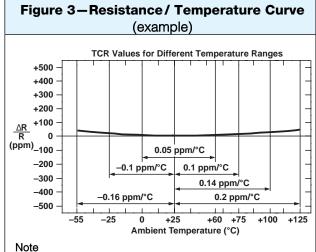
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Foil shown in black, etched spaces in white

#### Note

To acquire a precision resistance value, the Bulk Metal® Foil chip is trimmed by selectively removing built-in "shorting bars." To increase the resistance in known increments, marked areas are cut, producing progressively smaller increases in resistance. This method reduces the effect of "hot spots" and improves the long-term stability of Bulk Metal® Foil resistors.



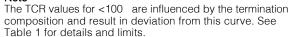


Table 4—Load Life Stability (+70°C for 2000 h)

**∆R** Limits ±0.005% at 50 mW

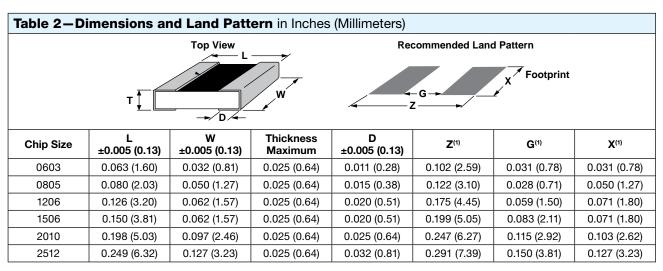
±0.01% at 100 mW ±0.005% at 100 mW

±0.01% at 200 mW ±0.005% at 150 mW

±0.01% at 300 mW ±0.005% at 200 mW

±0.01% at 500 mW ±0.005% at 500 mW

±0.01% at 750 mW



<sup>(1)</sup> Land Pattern Dimensions are per IPC-7351A

Table 3-Specifications						
Chip Size <sup>(1)</sup>	Rated Power at +70°C	Max. Working Voltage (≤√P×R)	Resistance Range <sup>(2)</sup>	Maximum Weight		
0603	100 mW	20 V	100 $\Omega$ to 5 k $\Omega$	3 mg		
0805	200 mW	40 V	5 $\Omega$ to 8 k $\Omega$	6 mg		
1206	300 mW	87 V	5 $\Omega$ to 25 k $\Omega$	11 mg		
1506	300 mW	95 V	5 $\Omega$ to 30 k $\Omega$	12 mg		
2010	500 mW	187 V	5 $\Omega$ to 70 k $\Omega$	27 mg		
2512	750 mW	220 V	$5~\Omega$ to 125 k $\Omega$	40 mg		

<sup>(1)</sup> For size 2018, please contact us using the e-mail address in the footer below.

<sup>(2)</sup> For non-standard values please contact application engineering

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Chip Size

0603

0805

1206, 1506

2010

2512

Page 2 of 5



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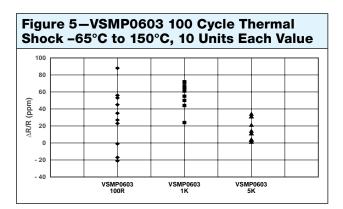
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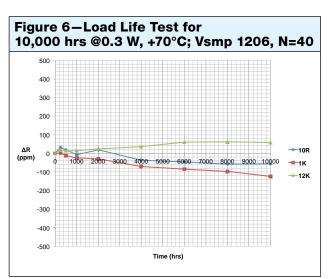
Table 5-Performances						
Test or Conditions	MIL-PRF-55342 Characteristic E $\Delta$ R Limits	Typical ∆R Limits	Performance ∆R Limits <sup>(1)</sup>			
Thermal Shock, 100× (-65°C to +150°C)	±0.1%	±0.005% (50 ppm)	±0.01% (100 ppm)			
Low Temperature Operation, -65°C, 45 min at P <sub>nom</sub>	±0.1%	±0.005% (50 ppm)	±0.01% (100 ppm)			
Short Time Overload, 6.25 × Rated Power, 5 s	±0.1%	±0.005% (50 ppm)	±0.01% (100 ppm)			
High Temperature Exposure, +150°C, 100 h	±0.1%	±0.01% (100 ppm)	±0.02% (200 ppm)			
Resistance to Soldering Heat	±0.2%	±0.005% (50 ppm)	±0.01% (100 ppm)			
Moisture Resistance	±0.2%	±0.005% (50 ppm)	±0.02% (200 ppm)			
Load Life Stability +70°C for 2000 h at Rated Power	±0.5%	±0.005% (50 ppm)	±0.01% (100 ppm)			

 $^{(1)}$  As shown +0.01  $\Omega$  to allow for measurement errors at low values

#### Figure 4—Recommended Mounting

- 1. IR and vapor phase reflow are recommended.
- 2. Avoid the use of cleaning agents that attack epoxy resins, which form part of the resistor construction.
- 3. Vacuum pick up is recommended for handling.
- 4. If the use of a soldering iron becomes necessary, precautionary measures should be taken to avoid any possible damage/overheating of the resistor.
- \* Recommendation: The solder fillet profile should be such as to avoid running over the top metallization.





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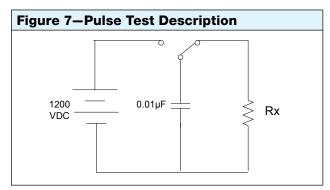
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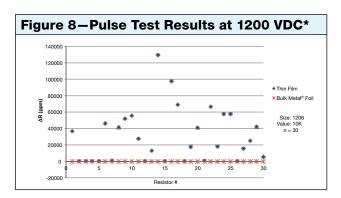
#### **PULSE TEST**

#### **Test Description**

All parts are baked at +125°C for 1 hour and allowed to cool at room temperature for 1 hour, prior to testing. By using an electrolytic 0.01  $\mu$ F capacitor charged to 1200 VDC, a single pulse was performed on 30 units of 1206, 10 k $\Omega$  of Surface Mount Bulk Metal® Foil resistor and Thin Film resistor. The units were allowed time to cool down, after which the resistance measurements were taken and displayed in ppm deviation from the initial reading.

#### **Test Results**





\* **Note** Average of 30 units yielded deviation of 30,723 ppm of the Thin Film vs. –14 ppm for the Bulk Metal® Foil

### **ELECTROSTATIC DISCHARGE (ESD)**

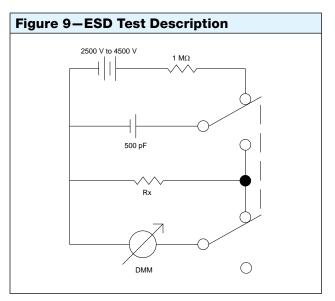
ESD can be categorized into three types of damages:

Parametric Failure – occurs when the ESD event alters one or more device parameters (resistance in the case of resistors), causing it to shift from its required tolerance. This failure does not directly pertain to functionality; thus a parametric failure may be present while the device is still functional.

<u>Catastrophic Damage</u> – occurs when the ESD event causes the device to immediately stop functioning. This may occur after one or a number of ESD events with diverse causes, such as human body discharge or the mere presence of an electrostatic field. Latent Damage – occurs when the ESD event causes moderate damage to the device, which is not noticeable, as the device appears to be functioning correctly. However, the load life of the device has been dramatically reduced, and further degradation caused by operating stresses may cause the device to fail during service. Latent damage is the source for greatest concern, since it is very difficult to detect by re-measurement or by visual inspection, because damage may have occurred under the external coating.

#### **Test Description**

By using an electrolytic 500 pF capacitor charged up to 4500 V, pulses were performed on 10 units of 1206, 10 k $\Omega$  of three different Surface Mount Chip Resistors technologies, with an initial voltage spike of 2500 V (Figure 10). The units were allowed time to cool down, after which the resistance measurements were taken and displayed in ppm deviation from the initial readings. Readings were then taken in 500 V increments up to 4500 V.



#### Table 6—ESD Test Results (Average of 10 Units) ΔR **Thick Film Thin Film** Bulk Metal Foil 2500 V -2.7% 97% <0.005% 3000 V 366% -4.2% <0.005% 3500 V -6.2% Open <0.005% 4000 V -7.4% Open <0.005% <0.005% 4500 V -8.6% Open

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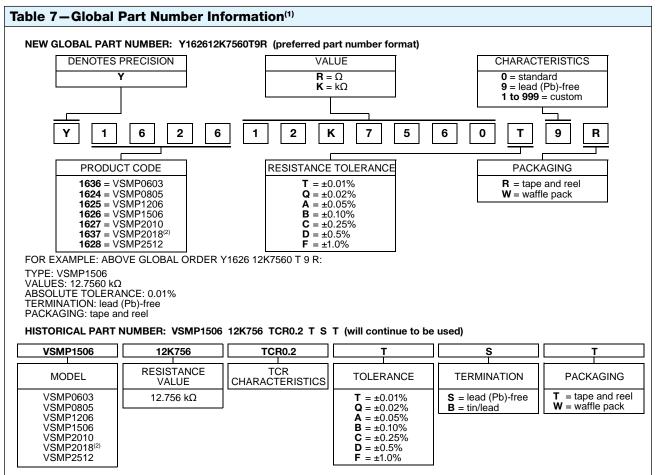
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<sup>(2)</sup> For size 2018, please contact us

### **CUSTOMIZED PRECISION AND NO MINIMUM ORDER QUANTITIES**

Texas Components provide quick delivery of Bulk Metal<sup>®</sup> Foil resistors in any ohmic value and no minimum order quantity, since Bulk Metal<sup>®</sup> Foil resistors are not restricted to standard values and each resistor is trimmed to the precise value ordered.

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