



## Introduction

Almost fifty years after its invention by physicist Dr. Felix Zandman in 1962, Bulk Metal® Foil technology still outperforms all other resistor technologies available today for applications that require precision, stability, and reliability. Vishay Precision Group (VPG) offers Bulk Metal® Foil products in a variety of resistor configurations and package types to meet the needs of a wide range of applications.

In 2000, Vishay Foil Resistors achieved a technological breakthrough with the introduction of Bulk Metal® Z-Foil. Products built on this revolutionary technology deliver an absolute temperature coefficient of resistance (TCR) of  $\pm 0.05$  ppm/°C (-55 °C to +125 °C, +25 °C ref.) and  $\pm 0.05$  ppm/°C (0 °C to +60 °C, +25 °C ref.), one order of magnitude better than previous Foil technologies. The lower the absolute TCR, the better a resistor can maintain its precise value despite ambient temperature variations and self-heating when power is applied.

By taking advantage of the overall stability and reliability of Vishay Foil resistors, designers can significantly reduce circuit errors and greatly improve overall circuit performance. Bulk Metal® technology allows Vishay Foil to produce customer-oriented products designed to satisfy challenging technical requirements. Customers are invited to contact our Application Engineering Department with non-standard technical requirements and special applications (email: [foil@vishaypg.com](mailto:foil@vishaypg.com)).

## Key Features

- Temperature coefficient of resistance (TCR) for Z-Foil technology
  - $\pm 0.05$  ppm/°C typical (0 °C to +60 °C, +25 °C ref.)
  - $\pm 0.2$  ppm/°C typical (-55 °C to +125 °C, +25 °C ref.)
- Power coefficient of resistance for Z-Foil technology (Power PCR)
  - “ $\Delta R$  due to self heating”:  $\pm 5$  ppm at rated power
- Load life stability: to  $\pm 0.005$  % (50ppm) at +70 °C, 10,000 hours at rated power
- Resistance tolerance: to  $\pm 0.001$  % (10 ppm)
- Resistance range: 0.5 m $\Omega$  to 3.3 M $\Omega$
- Electrostatic discharge (ESD) at least to 25, 000 V
- Non inductive, non capacitive design
- Rise time: 1 ns without ringing
- Thermal stabilization time < 1 sec (nominal value achieved within 10 ppm of steady state value)
- Current noise: 0.010  $\mu$ V (RMS)/Volt of Applied Voltage (< - 40 dB)
- Thermal EMF: 0.05  $\mu$ V/°C
- Voltage Coefficient: < 0.1 ppm/V
- Trimming operations increase resistance in precise steps but from remote locations so that the etched grid in the active area remains reliable and noise-free (see figures 4 and 5)
- Lead (Pb) free, Tin/Lead and Gold terminations are available

## Range of Foil Resistor Products

- Surface-mount chips, molded resistors and networks
- Power resistors and current sensors
- Military established reliability ( QPL, DSCC, EEE-INST-002, ESA, CECC)
- Leaded (Through-Hole)
- Hermetically-sealed
- Trimming potentiometers
- Voltage dividers and networks

- Hybrid chips (wire-bondable chips)
- High Temperature resistors (>220°C)
- Resistors for Audio

## Reason 1: Temperature Coefficient of Resistance (TCR)

“Why are extremely low absolute TCR resistors required?” is a proper question when evaluating the performance and cost of a system. The answers are as numerous as the systems in which they are installed. The following pages discuss ten different individual technical characteristics of the Bulk Metal® Foil technology that are important to precision analog circuits. While each characteristic is discussed independently for clarity, many circuits require some specific combination of these characteristics and, often, all characteristics are required in the same resistive devices. For example, one might examine the requirements of an operational amplifier.

In operational amplifiers the gain is set by the ratio of the feedback resistor to the input resistor. With differential amplifiers the Common Mode Rejection Ratio (CMRR) is based on the ratios of a four-resistor set. In both cases, any change in the ratios of these resistors directly affects the function of the circuit. The ratios might change due to different absolute temperature coefficients experiencing differential heating (either internal or external), differential tracking through changes in ambient temperature, differential time-response to step inputs or high frequency signals, differential Joule heating due to different power levels, different changes in resistance over design life, etc. So it can easily be seen that it is common for many circuits to depend on many application-related stability characteristics — all at the same time in the same devices. The Bulk Metal® Foil technology is the ONLY resistor technology that provides the tightest envelope of ALL of these characteristics in the same resistor devices, with low noise also coming along as inherent to the foil technology. Whether necessary for any specific application or not, all of these characteristics are inherent to the foil technology and all are automatically included in any of the foil resistors.

The solution to these problems is extremely low **absolute** TCR resistors to keep temperature-induced changes to a minimum.

### Initial TCR

Two predictable and opposing physical phenomena within the composite structure of the resistive alloy and its substrate are the key to the low absolute TCR capability of Bulk Metal® Foil resistor:

- Resistivity of the resistive alloy changes directly with temperature in a free air (Resistance of the foil increases when temperature increases.)
- The Coefficient of Thermal Expansion (CTE) of the alloy and the substrate to which the foil alloy is cemented are different resulting in a compressive stress on the resistive alloy when temperature increases. (Resistance of the foil decreases due to compression caused by the temperature increases.)

The TCR of the Foil resistor is achieved by matching two opposing effects - the inherent increase in resistance due to temperature increase vs. the compression - related decrease in resistance due to that same temperature increase. The two effects occur simultaneously resulting in an unusually low predictable, repeatable, and controllable TCR. Due to VPG’s Bulk Metal® Foil resistor design, this TCR characteristic is accomplished automatically, without selection, and regardless of the resistance value or the date of manufacture — even if years apart!

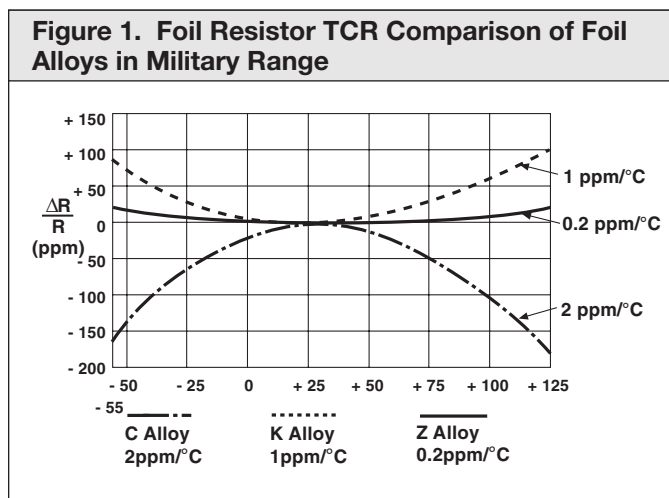
# Ten Technical Reasons to Specify Bulk Metal® Foil

## Vishay Foil Resistors

### Improved TCR In Bulk Metal® Z-Foil Resistors to $\pm 0.2 \text{ ppm}/^\circ\text{C}$

Foil resistor technology has continued to progress over the years, with significant improvements in TCR. Figure 1 shows the typical TCR characteristics of the various foil alloys utilized by Vishay Foil to produce Bulk Metal® Foil resistors.

The original Alloy C Foil exhibits a negative parabolic response to temperature with a positive chord slope on the cold side and a negative chord slope on the hot side.



Following was the Alloy K Foil which produced an opposite parabolic response with temperature with a negative chord slope on the cold side and a positive chord slope on the hot side. In addition, it provides a TCR approximately one half that of Alloy C Foil.

The latest development are the Alloys Z and Z1 Foil Technologies Breakthrough which has a similar parabolic response as the Alloy K Foil but produces TCR characteristics an order of magnitude better than Alloy C and five times better than Alloy K.

Using this technology, extremely low TCR resistors have been developed that provide virtually zero response to temperature.

These technological developments have resulted in a major improvement in TCR characteristics compared to what was available before, and what is available in any other resistor technology.

### Typical TCR

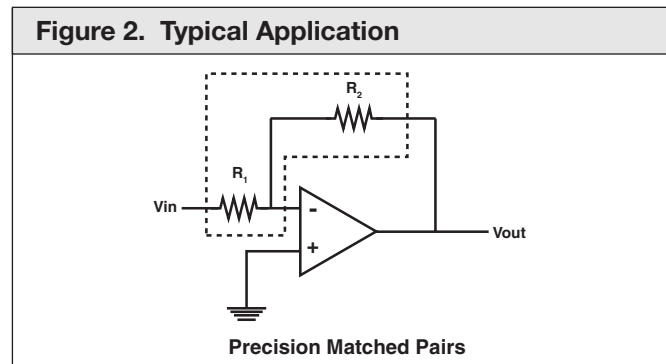
Foil typical TCR is defined as the chord slopes of the relative change of resistance vs temperature (RT) curve, and is expressed in ppm/°C (parts per million per degree centigrade). Slopes are defined from 0 °C to + 25 °C and + 25 °C to + 60 °C (Instrument Range); and from - 55 °C to + 25 °C and + 25 °C to + 125 °C (Military Range). These specified temperatures and the defined typical TCR chord slopes apply to all resistance values including low value resistors. Note, however, that without four terminals and Kelvin connections in low values, allowance for lead resistance and associated TCR may have to be made. All resistance and TCR measurements of leaded styles are made by the factory at a gage point 1/2" from the standoffs. Contact Applications Engineering Department for the TCR increase to be expected for low value resistors.

### TCR Tracking

“Tracking” is the stability of the ratio(s) of two or more resistors. When more than one resistor shares the same substrate (e.g. see Figure 2), the TCR tracking will be much better than the TCR provided by two discrete resistors. Resistors with different technologies increase or decrease in value when temperatures change also from the same batch. Resistance ratio tracking is influenced by heat that comes from outside the device (such as a rising ambient temperature or adjacent hotter objects) and from inside the device (as a result of self-heating due to power dissipation). Resistors may be selected for good TCR tracking when they are all at the same temperature. But, changes due to differential internal temperatures (e.g. differential power dissipation) or different local temperatures (e.g. differential heating from neighboring components) are superimposed upon the tracking and cause additional temperature - related errors. **Therefore, low absolute TCR is as important as tracking in precision applications.**

The best analog design would be using a fundamentally low absolute TCR resistor since it would minimize the effect of ambient temperature and self-heating.

This is impossible to accomplish with high TCR resistors > 5 ppm/°C even with good initial TCR Tracking of less than 2 ppm/°C.



### Reason 2: Power Coefficient of Resistance (PCR)

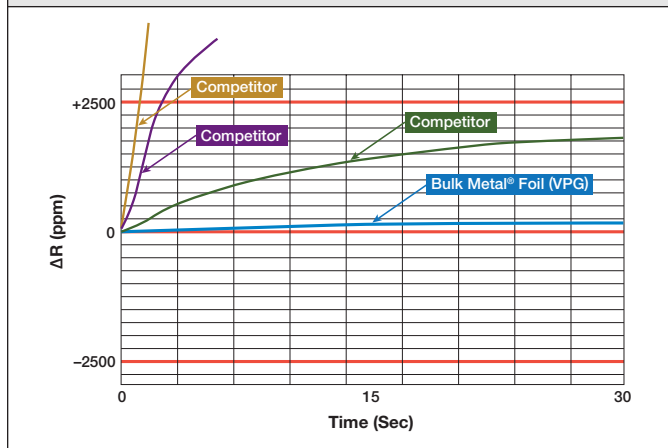
The TCR of a resistor for a given temperature range is established by measuring the resistance at two different ambient temperatures: at room temperature and in a cooling chamber or oven. The ratio of relative resistance change and temperature difference gives the slope of  $\Delta R/R = f(T)$  curve. This slope is usually expressed in parts per million per degree Centigrade (ppm/°C). In these conditions, a uniform temperature is achieved in the measured resistance. In practice, however, the temperature rise of the resistor is also partially due to self-heating as a result of the power it is dissipating (self heating). As stipulated by the Joule effect, when current flows through a resistance, there will be an associated generation of heat. Therefore, the TCR alone does not provide the actual resistance change for precision resistor. Hence, another metric is introduced to incorporate this inherent characteristic – the Power Coefficient of Resistance (PCR). PCR is expressed in parts per million per Watt or in ppm at rated power. In the case of Z-based Bulk Metal® Foil, the PCR is 5 ppm typical at rated power or 4 ppm per Watt typical for power resistors. For example: for Foil power resistor with TCR of 0.2 ppm/°C and PCR of 4 ppm/W, a temperature change of 50 °C (from + 25 °C to + 75 °C) at a power of 0.5 W will produce a  $\Delta R/R$  of  $50 \times 0.2 + 0.5 \times 4 = 12$  ppm absolute change.

### Reason 3: Thermal Stabilization

When power is applied to the resistor, self-heating occurs. Foil's low TCR and PCR capabilities help to minimize this effect. But to achieve high precision results, a rapid response to any changes in the environment or other stimuli is necessary. When the level of power is changed, one would desire the resistance value to adjust accordingly as quickly as possible. A rapid thermal stabilization is important in applications where the steady state value of resistance according to all internal and external factors must be achieved quickly to within a few ppm.

While most resistor technologies may take minutes for such speed and precision of thermal stabilization to its steady state value, foil resistor is capable of almost immediate stabilization, down to within a few ppm in under a second. The exact response is dependent on the ambient temperature as well as the change in power applied; the heat flow when power applied places mechanical stresses on the element and as a result causes temperature gradients. Regardless, foil outperforms all other technologies by a large margin (see Figure 3).

**Figure 3. Thermal stabilization demonstration (1 W applied to 50 mΩ SMD 2512)**



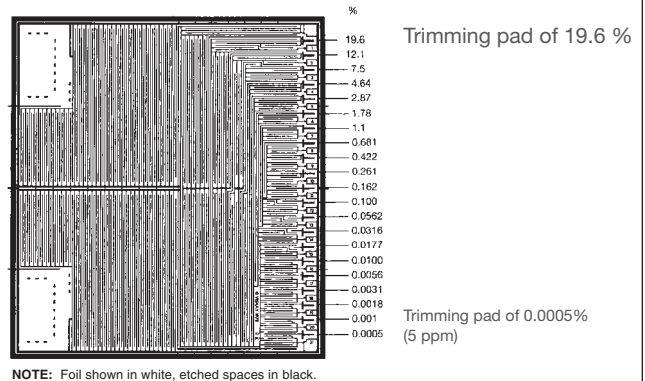
### Reason 4: Resistance Tolerance

Why do users employ tight tolerance resistors? A system or a device or one particular circuit element must perform for a specified period of time and at the end of that service period, it must still perform within specification. During its service life, it may have been subjected to some hostile working conditions and therefore may no longer be within purchased tolerance. One reason for specifying a tighter purchased tolerance than the end of life error budget tolerance is to allow room for service shifts. Another reason is that the error budget is more economically applied to resistors than to most other components.

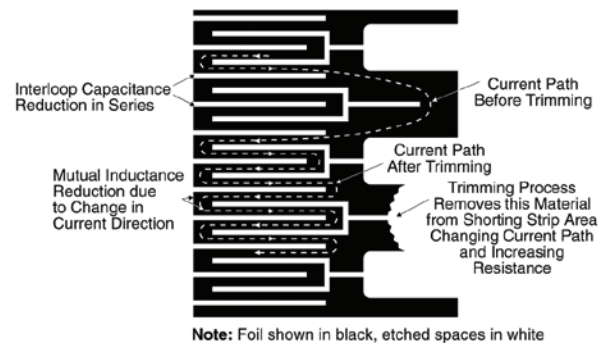
The Bulk Metal Foil resistors are calibrated as accurately as 0.001% by selectively trimming various adjusting points that have been designed into the photoetched pattern of the resistive element (see Figure 4). They provide predictable step increases in resistance to the desired tolerance level. Trimming the pattern at one of these adjusting points will force the current to seek another longer path, thus raising the resistance value of the element by a specific percentage. The trimming operations increase resistance in precise steps but from

remote locations so that the etched grid in the active area remains reliable and noise-free. In the fine adjust areas, trimming affects the final resistance value by smaller and smaller amounts down to 0.001% and finally 0.0005% (5 ppm). This is the trimming resolution (see Figure 5).

**Figure 4. Vishay Foil Resistors S102C Resistor Element**



**Figure 5. Trimming to Values (Conceptual Illustration)**



### Reason 5: Load-Life Stability

Why are designers concerned about stability with applied load? Load-life stability is the characteristic most relied upon to demonstrate a resistor's long-term reliability. Military testing requirements to 10,000 hours with limits on amount of shift and the number of failures results in a failure rate demonstration. Precision Bulk Metal® Foil resistors have the tightest allowable limits. Whether military or not, the load-life stability of foil resistors is unparalleled and long-term serviceability is assured.

The reason foil resistors are so stable has to do with the materials of construction (Bulk Metal® Foil and high alumina substrate). For example, the S102C and Z201 resistors are rated at 0.3 W at 125 °C with an allowable ΔR of 150 ppm max after 2000 hours under load and 500 ppm max after 10,000 hours. (See Figures 6 and 7 for the demonstrated behavior). Conversely, the ΔR is reduced by decreasing the applied power which lowers the element temperature rise in Vishay Foil resistors. Figure 6 shows the drift due to load-life testing



# Ten Technical Reasons to Specify Bulk Metal® Foil

## Vishay Foil Resistors



at rated power and Figure 7 shows the drift due to load-life testing at reduced power. Reducing the ambient temperature has a marked effect on load-life results and Figure 8 shows the drift due to rate power at different ambient temperatures. The combination of lower power and ambient temperature is shown in Figure 9 for model S102C.

Our engineers have ensured the stability of our resistors through several tests and experiments. Figure 10 displays the results of our test that has been in progress for 29 years. 50 sample S102C 10 kΩ resistors have been in a 70 °C heating chamber while under 0.1 W applied power for this entire duration. The average deviation in resistance is just 60 ppm.

Figure 11 shows Documented Self Life performances made by a customer for hermetically sealed VHP101 Foil resistors for over 8 years. The average deviation did not exceed 1 ppm.

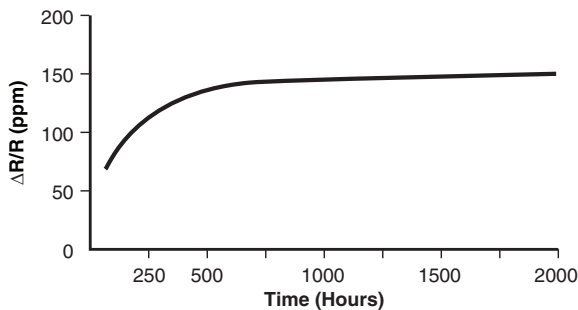
For evaluation of load-life stability, the two parameters which must be mentioned together, power rating and ambient temperature, can be joined into one single parameter for a given style of resistor. If the steady state temperature rise can be established, it can be added to the ambient temperature, and the sum will represent the combined (load induced + ambient) temperature. For instance, the Vishay Foil S102C resistor has a temperature rise of 9 °C per 0.1 W of applied power. This leads to the following example calculations:

If  $T = 75\text{ °C}$ ,  $P = 0.2\text{ W}$ , and  $t = 2000\text{ hrs.}$ ;  
Then self-heating =  $9\text{ °C} \times 2 = 18\text{ °C}$ .

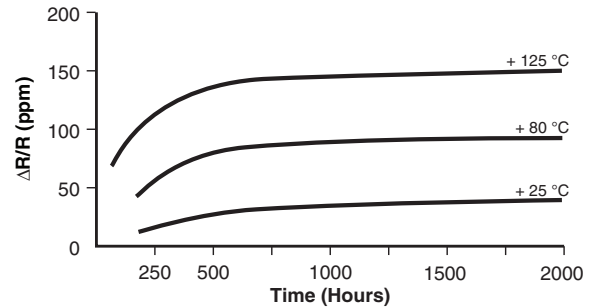
$18\text{ °C rise} + 75\text{ °C ambient} = 93\text{ °C total } \Delta R$ .

$R_{\text{max}} = 80\text{ ppm}$  from the curve of Figure 12.

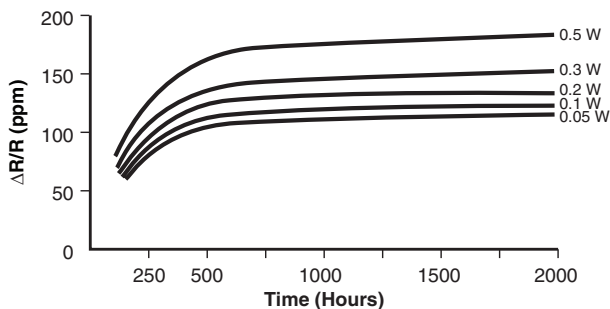
**Figure 6. Relative Resistance Change ( $\Delta R/R$ ) as a Function of Time, Load 0.3 W, +125 °C Ambient**



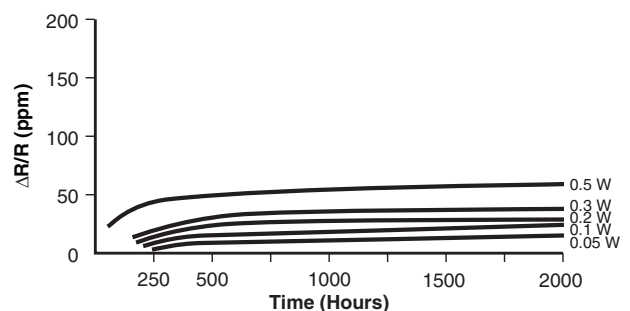
**Figure 8.  $\Delta R/R = F(\text{Time})$ , Loads 0.03W, Different Ambient Temperatures**



**Figure 7. ( $\Delta R/R$ ) = F (Time), Loads 0.05 to 0.5W, +125 °C Ambient**



**Figure 9.  $\Delta R/R = F(\text{Time})$ , Loads 0.05 to 0.5W, +25 °C Ambient**



**Figure 10. Long-Term Stability Over 29 Years, 0.1W at 70 °C, 50 Samples (S102C, 10KΩ)**

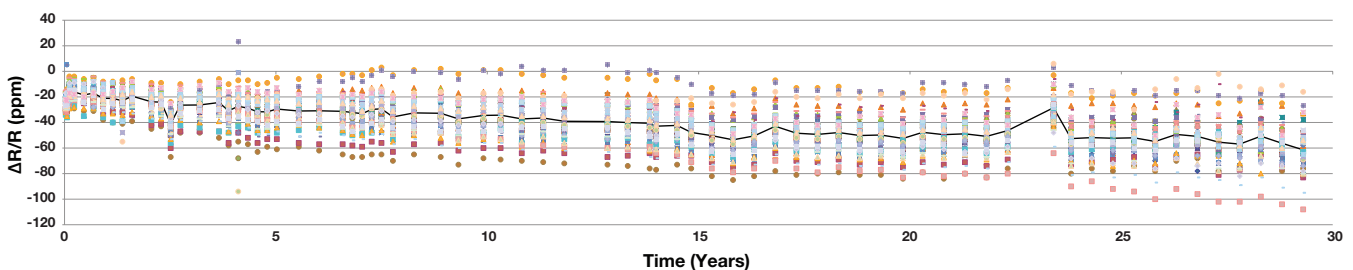
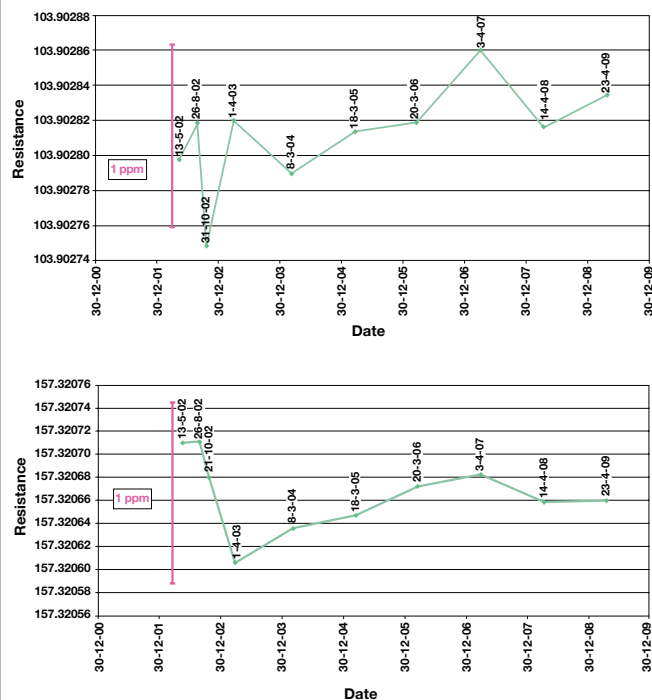


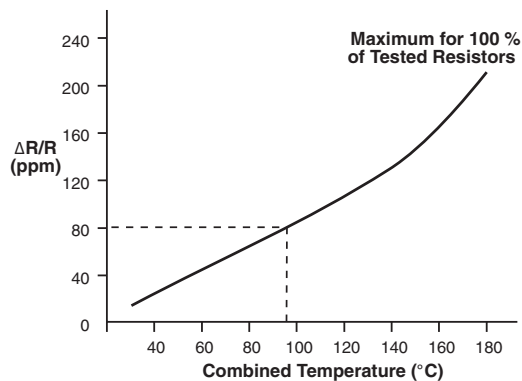
Figure 12 shows, for a given duration of load life test, how the drift increases with the level of the applied combined temperature. As explained above, the combined temperature comprises the effect of power induced temperature rise and the ambient temperature. The curve shows maximum drift.

**Figure 11. Shelf Life Test Results of Hermetically Sealed VHP101 Foil Resistors Over 8 Years**



**Figure 12. Maximum Resistance Shifts After 2000 Hrs. of Load Life Test Under Thermal Stresses\***

\* Combined temperature— ambient and temperature rise due to applied power.

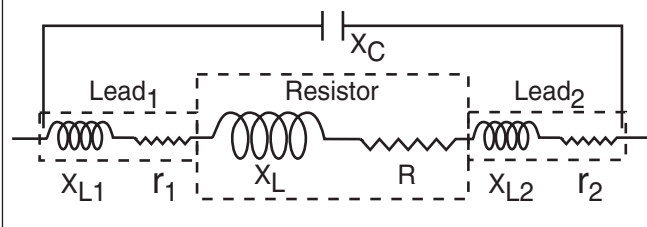


This information is based on product taken off the line without any screen testing or power conditioning. Further drift reduction is available by factory power conditioning. Consult Applications Engineering for this and other screening tests that are available.

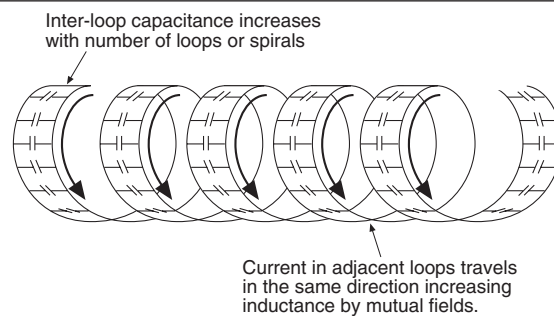
## Reason 6: High Speed and Response Time

The equivalent circuit of a resistor, as shown in Figure 13, combines a resistor in series with an inductance and in parallel with a capacitance (PLC). Resistors can perform like an R/C circuit, filter or inductor depending on their geometry. In spiraled and wirewound resistors, these reactances are created by the loops and spaces formed by the spirals or turns of wire. Figure 14 shows how the capacitance and inductance increase as the resistance value increases due to continually increasing the number of spirals or turns. Certain assembly techniques attempt to mitigate the inductance in wirewound resistors but all have only limited effect. One the other hand, in planar resistors such as the Bulk Metal® Foil resistors, the geometry of the lines of the resistor patterns is intentionally designed to counteract these reactances. Figure 15 shows a typical serpentine pattern of a planar resistor. Opposing current directions in adjacent lines reduces mutual inductance while geometry-related inter-line capacitances in series re-

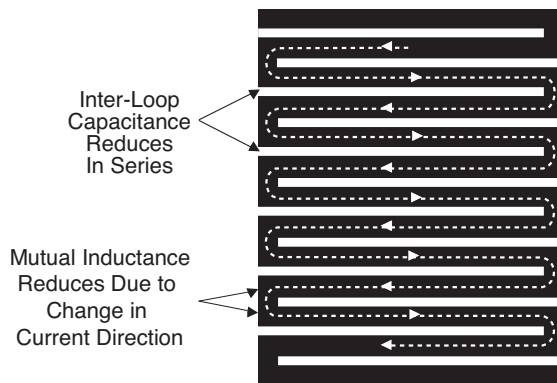
**Figure 13. The Equivalent Circuit of a Resistor**



**Figure 14. Capacitance and Inductance in a Wound or Spiraled Resistor**



**Figure 15. Bulk Metal® Foil Planar Design**

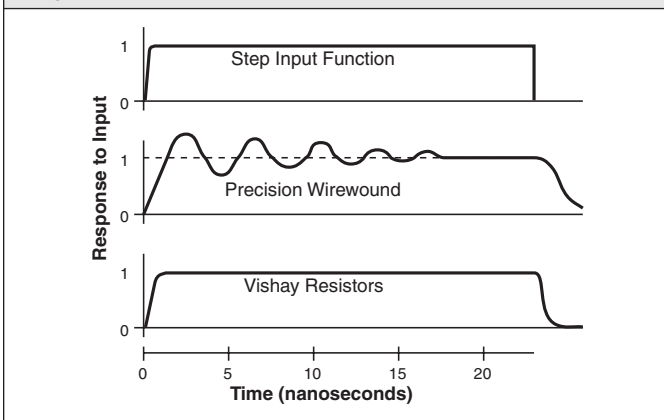


duces overall capacitance. Both inductance and capacitance produce reactance proportional to the operating frequency and it changes the effective resistance and the phase between the current and voltage in the circuit.

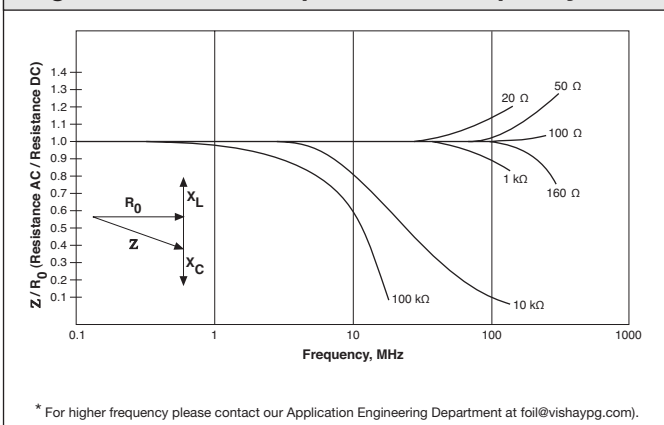
Both inductive and capacitive reactances distort input signals, particularly in pulse applications. Figure 16 shows the current response to a voltage pulse comparing a fast Bulk Metal® Foil resistor to a slower wirewound resistor. Here a pulse width of one nanosecond would have been completely missed by the wirewound resistor while the foil resistor achieves full replication in the time allotted.

In frequency applications, these reactive distortions also cause changes in apparent resistance (impedance) with changes in frequency. Figure 17 shows a family of curves relating the AC resistance to the DC resistance in Bulk Metal® Foil resistors. Very good response is seen in the 100 Ω range out to 100 MHz and all values have good response out to 1 MHz. The performance curves for other resistor technologies can be expected to show considerably more distortion (particularly wirewounds).

**Figure 16. Comparison of a Response to a Pulse**



**Figure 17. Effect of Operation at Frequency\***



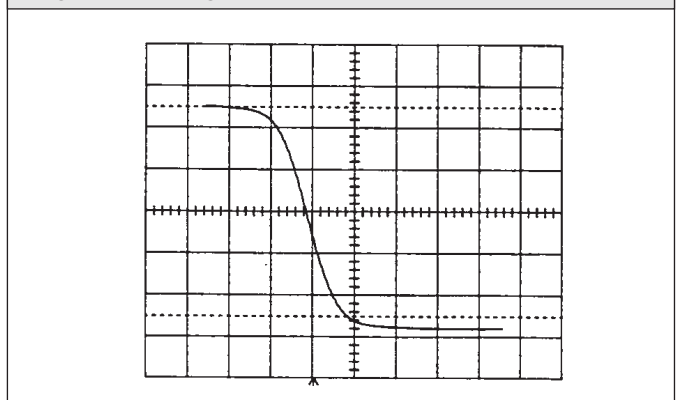
## Reason 7: Noise: “Hear The Difference”

As sound reproduction requirements become more demanding, the selection of circuit components become more exacting and the resistors in the signal path are critical. Measurement instrumentation based on low level signal inputs and high gain amplification cannot tolerate microvolt level background noise when the signal being measured is itself in the microvolt range. Although audio circuitry, where signal purity is of utmost concern, is the most obvious use of noise-free components, other industries and technologies are equally concerned with this characteristic.

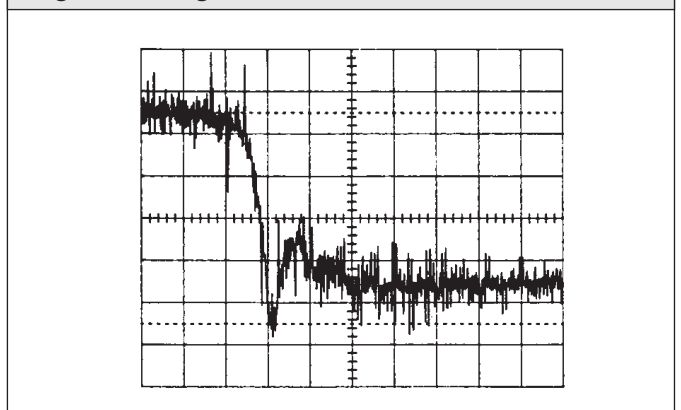
Resistors, depending on construction, can be a source of noise. This unintended signal addition is measurable and independent of the presence of a fundamental signal.

Figures 19 and 20 illustrate the effects of resistor noise on a fundamental signal. Resistors made of conductive particles in a non-conductive binder are the most likely to generate noise. In carbon composition and thick film resistors, conduction takes place at points of contact between the conductive particles within the binder matrix. Where these point-to-point contacts are made constitutes a high resistance conduction site which is the source for noise. These sites are sensitive to any distortion resulting from expansion mismatch, moisture swelling, mechanical strain, and voltage input levels. The response to these outside influences is an unwanted signal as the current finds its way through the matrix. Figure 20 illustrates this current path.

**Figure 18. Segment of a Fundamental Curve**



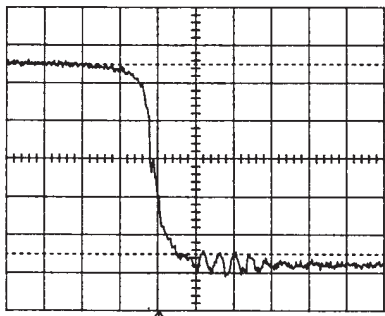
**Figure 19. Signal with Added Resistor Noise**



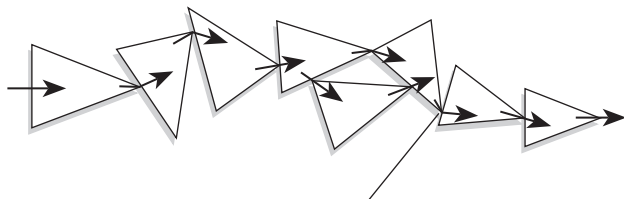
Resistors made of metal alloys, such as the Bulk Metal® Foil resistors, are the least likely to be noise sources. Here the conduction is across the inter-granular boundaries of the alloy. The intergranular current path from one or more metal crystals to another involves multiple and long current paths through the boundaries reducing the chance for noise generation. Figure 22 illustrates this current path.

In addition, the photolithography and fabrication techniques employed in the manufacture of Bulk Metal® Foil resistors results in more uniform current paths than found in some other resistor constructions. Spiraled resistors, for example, have more geometric variations that contribute to insertion of noise signals. Bulk Metal® Foil resistors have the lowest noise of any resistor technology, with the noise level being essentially immeasurable. Signal purity can be a function of the selection of resistor technology for pre-amp and amplifier applications. Vishay Foil Resistors offer the best performance for low noise audio applications.

**Figure 20. Signal with a Vishay Foil Resistor**

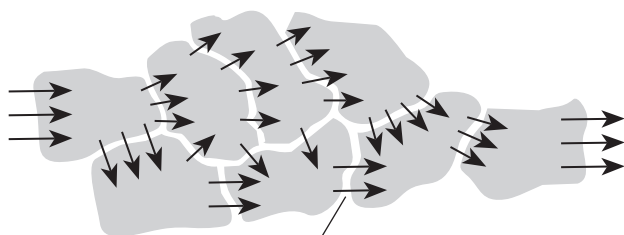


**Figure 21. The Current Path in a Particle-to-Particle Matrix**



Noise generation is maximum when current flow is through point to point contacts as shown in a particle to particle matrix.

**Figure 22. The Current Path in a Resistive Alloy**



Noise generation is minimal when current flow is through multiple paths as exists in Bulk Metal foil resistive alloy.

## Reason 8: Thermal EMF

When a junction is formed by two dissimilar metals, and is heated, a voltage is generated due to the different levels of molecular activity within these metals. This electromotive force, induced by temperature, is called Thermal EMF and is usually measured in microvolts. A useful purpose of this Thermal EMF is the measurement of temperature using a thermocouple and microvolt meter.

In resistors, this Thermal EMF is considered a parasitic effect interfering with pure resistance (especially at low values when DC is applied). It is often caused by the dissimilarity of the materials used in the resistor construction especially at the junction of the resistor element and the lead materials. The Thermal EMF performance of a resistor can be degraded by external temperature difference between the two junctions, dissymmetry of power distribution within the element, and the dissimilarity of the molecular activity of the metals involved.

One of the key features feature of the Vishay Foil resistor is its low Thermal EMF design. The flattened paddle leads (in through hole design) make intimate contact with the chip thereby maximizing heat transfer and minimizing temperature variations. The resistor element is designed to uniformly dissipate power without creating hot spots and the lead material is compatible with the element material. These design factors result in a very low Thermal EMF resistor.

Figures 23 and 24 display the various design characteristics that give these resistors an extremely low thermal EMF.

## Reason 9: Electrostatic Discharge (ESD)

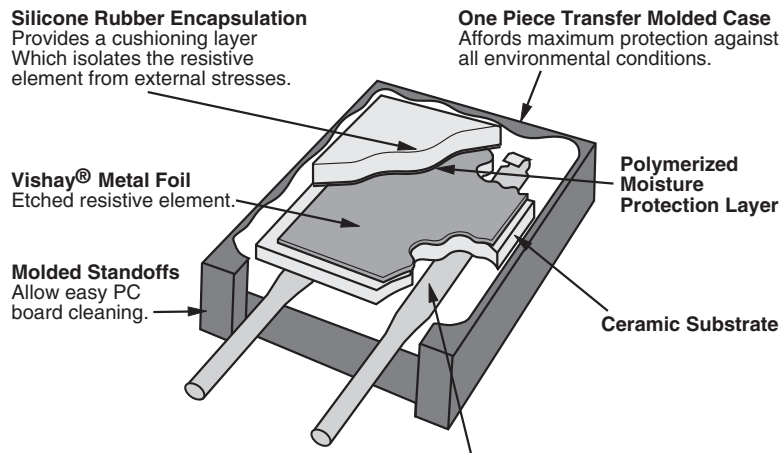
Electrostatic discharge (ESD) can be defined as a rapid transfer of charge between bodies at different electrical potentials – either by direct contact, arcing, or induction – in an attempt to become electrically neutral. The human threshold for feeling an ESD is 3000 V, so any discharge that can be felt is above this voltage level. Because the duration of this high voltage spike is less than a microsecond long, the net energy is small compared to the size of the human body over which it is spread. From the human body's point of view, ESD does no harm. But when the discharge is across a small electronic device, the relative energy density is so great that many components can be damaged by ESD at levels as low as 3000 V or even 500 V.

ESD damage is generally divided into three categories:

- Parametric Failure – the ESD event alters the resistance of the component causing it to shift from its required tolerance. This failure does not directly pertain to functionality; thus a parametric failure may be present even if the device is still functional.
- Catastrophic Damage – the ESD event causes the device to immediately stop functioning. This may occur after one or a number of ESD pulses, and may have many causes, such as human body discharge or the mere presence of an electrostatic field.
- Latent Damage – the ESD event causes moderate damage to the device, which is not noticeable, as the device appears to be functioning correctly. However, the load life of the device is dramatically reduced, as further degradation caused by operating stresses may cause the device to fail during service. This defect is of greatest concern as it is very difficult to detect by visual inspection or re-measurement.



**Figure 23. Ruggedized Construction**

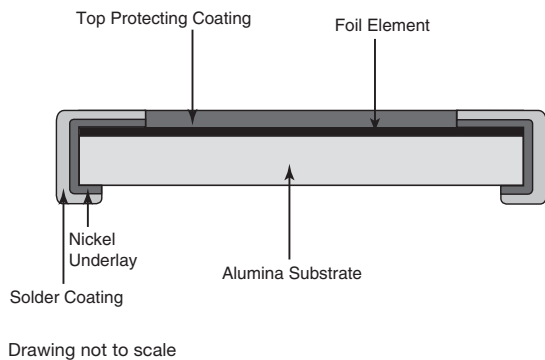


**Paddle Leads with Welded Terminations— No Ribbons**  
Only two welds, both remote from the lead-to-case point-of-entry, the best arrangement for maximum reliability. Excellent moisture resistance, high temperature, and load-life capabilities, low Thermal EMF.

The combination of ruggedized leads and molded case, plus the highly efficient heat transfer characteristics of the unique assembly and the ceramic substrate results in a high reliability resistor with excellent moisture resistance, high temperature, and load-life capabilities. These also afford a very low Thermal EMF.

Flattened “paddles” are wrapped around the resistance element structure and welded directly to the resistance alloy – thus there is only one weld per lead. The closely related thermal characteristics of the selected materials, combined with the unique “paddle” lead design, produce a resistor with extremely low Thermal EMF.

**Figure 24. Surface-Mount Wrap-Around Chip Foil Resistor Construction**



In resistors, ESD sensitivity is a function of their size. The smaller the resistor, the less space there is to spread the energy pulsed through it from the ESD. This energy concentration in a small area of a resistor's active element causes it to heat up, which could lead to irreversible damage. With the growing trend of miniaturization, electronic devices, including resistors, are becoming smaller and smaller, causing them to be more prone to ESD damage.

Thus, the superiority of Bulk Metal® foil precision resistors over Thin Film resistors, when subjected to ESD, is attributed mainly to their greater thickness (foil is 100 times thicker than Thin Film), and therefore the heat capacity of the resistive foil layer is much higher

compared to the thin film layer. Thin Film is created through particle deposition processes (evaporation or sputtering), while foil is a bulk alloy with a crystalline structure created through hot and cold rolling of the melt.

Tests performed have indicated that foil chip resistors can withstand ESD events at least to 25,000 V (data available), while Thin Film and Thick Film chip resistors have been seen to undergo catastrophic failures at electric potentials as low as 3000 V (parametric failures at even much less). If the application is likely to confront the resistor with ESD pulses of significant magnitude, the best resistor choice is Bulk Metal® Foil.

## Reason 10: Non-Measurable Voltage Coefficient

As mentioned earlier in our section on resistor noise, resistors can change value due to applied voltage. The term used to describe the rate of change of resistance with changing voltage is known as voltage coefficient. Resistors of different constructions have noticeably different voltage coefficients. In the extreme case, the effect in a carbon composition resistor is so noticeable that the resistance value varies greatly as a function of the applied voltage. Bulk Metal® Foil resistor elements are insensitive to voltage variation and the designer can count on foil resistors having the same resistance under varying circuit voltage level conditions. The inherent bulk property of the metal alloy provides a non-measurable voltage coefficient.

### Conclusion

#### All In One Resistor

The ten reasons to specify Foil resistors are inherent in the design and are not a function of manufacturing variables or a selection process. This combination of parameters is not available in any other resistor technology. Vishay Foil Resistors provides a unique, inherent combination of performance characteristics resulting in unmatched performance and high reliability, satisfying the needs of today's expanding requirements.

### Special Order

Consider Vishay Foil Resistors for all of your low TCR needs. Special orders may be placed for low TCR, low value resistors, and tight TCR tracking of individual resistors and network combinations. Contact the Applications Engineering Department to discuss your requirements for these and any other TCR applications (**email: [foil@vishaypg.com](mailto:foil@vishaypg.com)**).